

Applicant(s)	J. D. Beasom
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Group Art Unit	2814
Examiner Name	Howard Weiss
Attorney Docket No.	125.009US01

**INFORMATION DISCLOSURE
STATEMENT
FORM PTO-1449**

Title: A LATERAL MOSFET STRUCTURE OF AN INTEGRATED CIRCUIT HAVING
SEPARATED DEVICE REGIONS

Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Patent No.	Issue Date	Name	Filing Date
<i>J. D. W.</i>	5,264,719	11/23/93	Beasom	05/24/91
	5,482,888	01/09/96	Hsu et al.	08/12/94
	5,918,137	06/29/99	Ng et al.	04/27/98

Foreign Patent References				
Examiner Initials	Foreign Patent		Name	Publication Date
	Country	No.		T
<i>J. D. W.</i>	EP	0309828		04/05/89
	GB	2215515		09/20/89

Other References				
Examiner Initials	Author, Title, Date, Pages, etc.			
	NONE			

Examiner Signature	<i>J. D. W.</i>	Date Considered	8/18/04
*Examiner Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce